



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2811
Examiner: Ori Nadav
Confirmation No.: 2358

In Re PATENT APPLICATION Of:

Applicant: Hiroaki Nakano)
Serial No.: 10/756,366)
Filed: January 14, 2004) AMENDMENT
For: CIRCUIT BOARD FOR MOUNTING)
A SEMICONDUCTOR CHIP AND)
MANUFACTURING METHOD THEREOF)
Attny Ref.: TAI 146)

November 8, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is timely filed in response to the Official Action mailed on August 16, 2005. No fee is due. However, please charge our Deposit Account No. 18-0002 if any fees are needed to enter this paper, and please advise us accordingly. It is noted that no petition is required because of the authorization to charge, but please consider this paper a petition for extension of time if needed.

The amendments are supported in the disclosure, e.g., Fig. 3C.

New claims 20-22 are supported in the drawing and specification and by claim construction from claims 4, 7, and 10. They are patentable for the reasons below.

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fee to our Deposit Account
No. 18-0002